EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3864	(buffer or driver or (output adj1 circuit)) and (((series or serial$3) NEAR2 transistor$1) SAME capacitor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/10 11:33
L2	84	1 and ((reference or bias) adj2 voltage) and (protect$3 NEAR1 transistor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/10 11:21
L3	142	1 and (((reference or bias) adj2 voltage) SAME (invert$3 WITH capacitor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/10 11:32
L4	0	(buffer or driver or (output adj1 circuit)) SAME ((series or serial$3) NEAR2 transistor$1) SAME capacitor SAME ((reference or bias) adj2 voltage) SAME invert$3 SAME (protect$3 NEAR2 transistor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/10 11:36
L5	8	(buffer or driver or (output adj1 circuit)) SAME transistor$1 SAME capacitor SAME ((reference or bias) adj2 voltage) SAME invert$3 SAME (protect$3 NEAR2 transistor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/10 11:37

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(buffer or driver or (output adj1 circuit)) SAME transistor$1 SAME capacitor SAME ((reference or bias) adj2 voltage) SAME invert$3 SAME (protect$3 NEAR2 transistor)	US-PGPUB	OR	OFF	2006/10/10 11:39
L2	2	((buffer or driver or (output adj1 circuit)) SAME transistor$1 SAME capacitor SAME ((reference or bias) adj2 voltage) SAME invert$3 SAME (series or serial$2)).clm.	US-PGPUB	OR	OFF	2006/10/10 11:40
L3	4	((buffer or driver or (output adj1 circuit)) SAME transistor$1 SAME capacitor SAME ((reference or bias) adj2 voltage) SAME (invert$3 NEAR1 output)).clm.	US-PGPUB	OR	OFF	2006/10/10 11:41